

FIG. 1A (PRIOR ART)

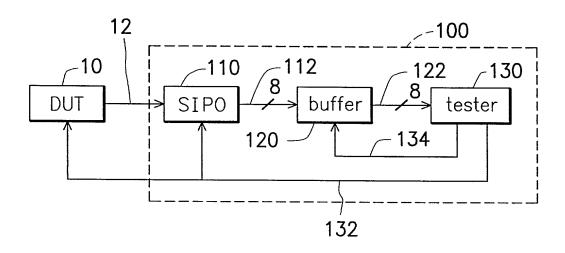


FIG. 1B (PRIOR ART)

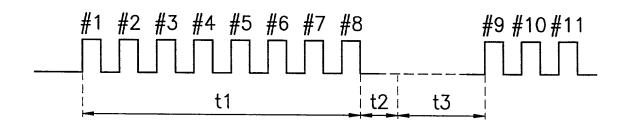


FIG. 2A (PRIOR ART)

(corresponding time interval)

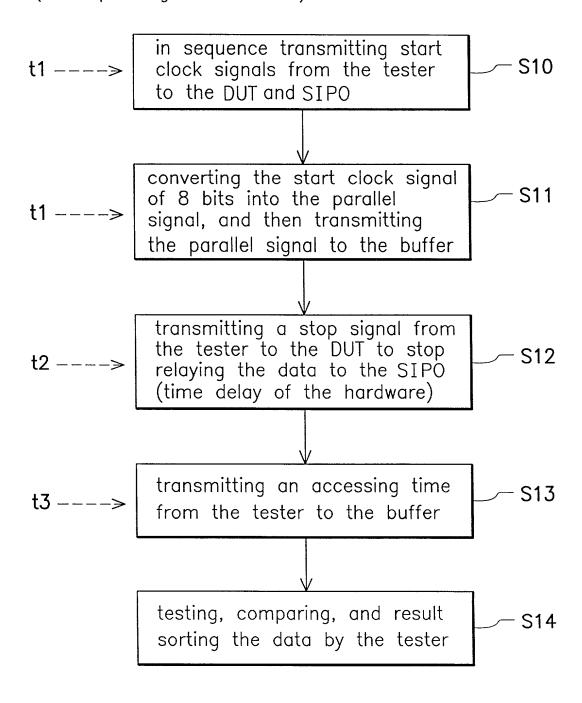


FIG. 2B (PRIOR ART)

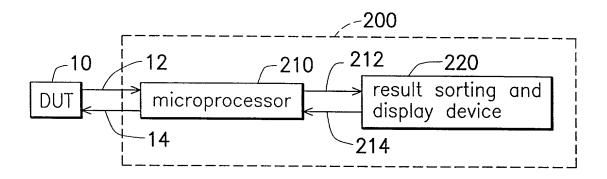


FIG. 3

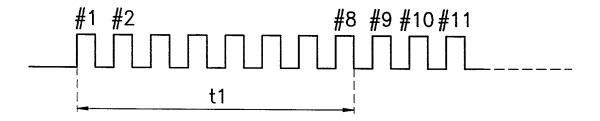


FIG. 4A

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(corresponding time interval) driving the microprocessor by the S20 resultsorting and display device transmitting the clock signal from the microprocessor to the DUT and outputting the stored data S21 receiving the data by the **S22** microprocessor, testing and comparing the data transmitting the result to the result sorting and display device, and sorting the result S23

FIG. 4B

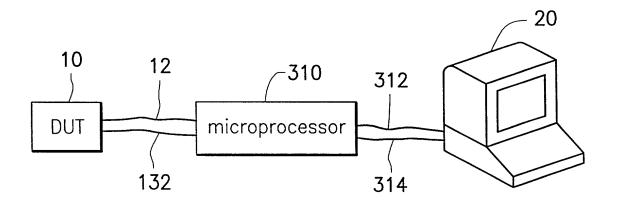


FIG. 5